August 2023

List of Publications

P. P. Vaidyanathan, California Institute of Technology

Reviewed journal articles

1) P. P. Vaidyanathan and S. K. Mitra, "On the construction of a digital transfer function from its real part on the unit circle," Proc. of the IEEE, vol. 70, pp. 198-199, February 1982 *correspondence item*.
2) P. P. Vaidyanathan and S. K. Mitra, "Low passband sensitivity digital filters: A generalized viewpoint and synthesis procedures," Proceedings of the IEEE, vol. 72, pp. 404-423, April 1984.
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4) P. P. Vaidyanathan, "On maximally flat linear phase FIR filters," IEEE Trans. on Circuits and Systems, vol. CAS-31, pp. 830-832, September 1984 (*correspondence item*).
5) P. P. Vaidyanathan, "On error spectrum shaping in state space digital filters," IEEE Trans. on Circuits and Systems, vol. CAS-32, pp. 88-92, January 1985.
6) P. P. Vaidyanathan, "The doubly terminated lossless digital two-pair in digital filtering," IEEE Trans. on Circuits and Systems, vol. CAS-32, pp. 197-200, February 1985.
7) P. P. Vaidyanathan, "Efficient and multiplierless design of FIR filters with very sharp cutoff via maximally flat building blocks," IEEE Trans. on Circuits and Systems, vol. CAS-32, pp. 236-244, March 1985.
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9) P. P. Vaidyanathan and S. K. Mitra, "Very low sensitivity FIR filter implementation using *structural passivity* concept," IEEE Trans. on Circuits and Systems, vol. CAS-32, pp. 360-364, April 1985.
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12) P. P. Vaidyanathan, "A unified approach to orthogonal digital filters and wave digital filters, based on LBR two-pair extraction," IEEE Trans. on Circuits and Systems, vol. CAS-32, pp. 673-686, July 1985.
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stopbands," IEEE Trans. on Circuits and Systems, vol. CAS-32, pp. 904-917, September 1985.

15) P. P. Vaidyanathan , "The discrete-time bounded real lemma in digital filtering," IEEE Trans. on Circuits and Systems, vol. CAS-32, pp. 918-924, September 1985.

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